

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2	"4860166".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:41
L2	1	(resistors! with conduct\$3 with board with ((integrated near circuit) ic) with package).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:41
L3	0	((IC or (integrated near circuit) or chip) with package with resistor with circuit near board with package with temperature near control\$5).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:42
L4	0	(resistors! with circuit near board with package with temperature).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:44
S1	7	347/18.ccls. and resistors! with conduct\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:26
S2	1	347/18.ccls. and resistors! and conduct\$4 and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:31
S3	2	347/18.ccls. and resistors! and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:34
S4	4	347/18.ccls. and resistors and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
S5	21	347/17.ccls. and resistors and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
S6	12	347/17.ccls. and resistors! and circuit near board	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35
S7	12	347/17.ccls. and resistors! and circuit near board and temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:35

## EAST Search History

S8	1	347/17.ccls. and resistors! and circuit near board and package	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:36
S9	360	resistors! same circuit near board same package and temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:43
S10	43	resistors! same circuit near board same package same temperature	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:38
S11	15	resistors! same circuit near board same package same temperature and temperature near control\$4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:43
S12	1	IC near package and resistor with circuit near board with package and temperature near control\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/07/24 14:44
S13	16	(IC or (integrated near circuit) or chip) with package and resistor with circuit near board with package and temperature near control\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/05/05 11:42